UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/797,000	03/11/2004	Yoshinori Ogawa	12480-000040/US	4643	
30593 7590 12/10/2007 HARNESS, DICKEY & PIERCE, P.L.C. P.O. BOX 8910 RESTON, VA 20195			EXAMINER		
			AMADIZ, RODNEY		
			ART UNIT	PAPER NUMBER	
			2629		
					
			MAIL DATE	DELIVERY MODE	
			12/10/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)			
Office Action Summary		10/797,000	OGAWA ET AL.			
	<i></i>	Examiner	Art Unit			
	The MAILING DATE of this communication app	Rodney Amadiz	2629 h the correspondence address			
Period for Reply						
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATES of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. It is period for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing end patent term adjustment. See 37 CFR 1.704(b).	TE OF THIS COMMUNIC, 6(a). In no event, however, may a regil apply and will expire SIX (6) MONTI cause the application to become ABA	ATION. bly be timely filed HS from the mailing date of this communication. INDONED (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 10 September 2007.					
• —	This action is FINAL . 2b) ☐ This action is non-final.					
3)∐	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims					
5)□ 6)⊠ 7)□	Claim(s) 1-11 is/are pending in the application. 4a) Of the above claim(s) is/are withdray Claim(s) is/are allowed. Claim(s) 1-11 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or					
Applicat	ion Papers					
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examine	epted or b) objected to b drawing(s) be held in abeyand on is required if the drawing(s	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(d).			
Priority (ınder 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
2) Notice	et(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO/SB/08)	Paper No(s)	ummary (PTO-413) /Mail Date formal Patent Application			
	er No(s)/Mail Date	6) 🔲 Other:	<u>_</u> .			

10/797,000 Art Unit: 2629

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-4, 8, 10 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakao (USPGPUB 2001/0003431—hereinafter "Nakao") in view of Liaw et al. (U.S. Patent 6,593,934—hereinafter "Liaw").

As to Claim 1, Nakao teaches a display device, comprising: a display panel (Fig. 6, Reference Number 1) including a plurality of pixels provided in matrix in a first direction and in a second direction (Fig. 7, A), the second direction intersecting with the first direction (Fig. 7, see 15 intersecting 14); a driving section (Fig. 6, Reference Numbers 3, 4 and 5 and Pg. 1, ¶'s 5-8) for sequentially driving, in the second direction, each pixel line provided along the first direction, the driving section causing the display panel to display an image that is in accordance with display data (Pg. 1, ¶'s 5-8); a reference voltage generating section (Fig. 1, 41) for generating reference voltages (Fig. 1, R0-R7) that represent multiple gradations (Pg. 4, ¶ 64 and 65), the reference voltages being used for displaying the image in the multiple gradations (Pg. 4, ¶ 64 and 65); a gamma-correction adjustment section (Fig. 1, 42 and Fig. 2) for adjusting the reference voltages (Fig. 1, R0-R7) so as to perform gamma-correction of the display data (Pg. 2, ¶ 22, Pg. 3, ¶ 46, Pg. 4, ¶ 65 and Pg. 5, ¶ 76); and controlling

10/797,000 Art Unit: 2629

the gamma-correction adjustment section so as to change the reference voltages on which the gamma-correction has been performed (Pg. 2, ¶ 21, 25, Pg. 6, ¶'s 88-91 and Pg. 7, ¶ 99) in accordance with the gamma correction adjustment data supplied to terminals different from terminals via which the display data is supplied (See Fig. 10 and note separate terminals "D" and "VR", furthermore note that element 39 is replaced with element 41 as per Pg. 4, ¶'s 63 and 64), and decreasing display unevenness between pixels that are adjacent to one another in at least one of the first and the second directions (inherent because gamma corrections are being altered according to the liquid crystal material (Pg. 2, ¶ 18); therefore if two adjacent pixels have different materials then a gamma correction is made so that both pixels output the same optical characteristics—See also Pg. 6, ¶ 96).

Nakao, however, fails to teach a memory for separately storing gamma correction adjustment data and display data and the gamma correction adjustment data being supplied from terminals different from terminals via which the display data is supplied. Examiner cites Liaw to teach a memory (*Fig. 1, 21*) for separately storing gamma correction adjustment data and display data (*Col. 6, lines 21-56*). At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to provide a memory that separately stores two types of data as taught by Liaw in the display device taught by Nakao in order to reduce the size of the display by consolidating to form a single memory.

Although Nakao teaches a controller (*Fig. 6, 5*), he fails to teach that it is the device that controls the gamma-correction adjustment section so as to change the

10/797,000 Art Unit: 2629

reference voltages on which the gamma-correction has been performed in accordance with the gamma correction adjustment data. Examiner cites Liaw to teach a control section for controlling a gamma-correction adjustment section so as to change the reference voltages on which the gamma-correction has been performed in accordance with the gamma correction adjustment data (Col. 6, line 57—Col. 7, line 31). At the time the invention was made it would have been obvious to a person of ordinary skill in the art to replace the Controller taught by Nakao with the controller taught by Liaw in order to have one device which manages the operations of the display and effectively communicates with the entire display system.

Finally, Nakao, as modified by Liaw fails to teach that the memory section is rewritable and nonvolatile. Examiner takes Official Notice that rewritable, nonvolatile memories are old and well-known in the art. At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to utilize a rewritable, nonvolatile memory in the display device taught by Nakao, as modified by Liaw, in order to prevent data loss while possessing the ability to update the display devices parameters.

As to <u>Claim 2</u>, Liaw teaches that the memory section is provided in the control section (*Liaw—See Fig. 7A, 20*).

As to <u>Claim 3</u>, Nakao, as modified by Liaw teaches that the memory section is provided in the driving section (Note that the combination of Nakao and Liaw yields the result of having the memory section in the driving section—refer to claim 1).

10/797,000 Art Unit: 2629

As to <u>Claim 4</u>, Nakao teaches that the display panel is divided into a plurality of display regions aligned in the first direction (*Fig. 6, 3—note that each source driver controls a different display region*); and the driving section includes a plurality of drivers for driving the plurality of display regions respectively (*Fig. 6, 3*).

As to <u>Claim 8</u>, Nakao teaches that the display panel includes: a thin-film transistor panel (*Fig. 6, 1*) including (i) a plurality of pixel electrodes (*Fig. 7, 11*) and (ii) thin-film transistors (*Fig. 7, 13*) respectively for the plurality of pixel electrodes (*See Fig. 7*); and an opposed panel on which opposed electrodes are provided (*Fig. 6, 2*); and the thin-film transistor panel and the opposed panel are provided in an overlapping manner so that an electrode formation surface of the thin-film transistor panel and an electrode formation surface of the opposed panel face one another (*See Fig. 6 and Pg. 1*, ¶'s 3-8).

As to <u>Claim 10</u>, Nakao, as modified by Liaw, teaches that the memory section is provided in the plurality of drivers (*Note that for the display to work properly each driver must be provided with the proper data (i.e. display data and gamma correction adjustment data)).*

As to <u>Claim 11</u>, all of the claim limitations have been addressed with respect to claim 1. Note that the claim language of claim 1 is more specific than the claim language in claim 11. Therefore all the "means" of claim 11 have already been addressed in claim 1.

10/797,000 Art Unit: 2629

3. Claims 6, 7 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakao and Liaw in view of Applicant's Admitted Prior Art (herein after AAPA).

As to <u>Claim 6</u>, Nakao, as modified by Liaw, fails to teach that the display panel includes a plurality of separate display panels provided in a surface direction of the display panel. Examiner cites AAPA to teach a display panel including a plurality of separate display panels provided in a surface direction of the display panel (See Fig. 13 and Pg. 3, second to last paragraph—Pg. 5, second to last paragraph). At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to incorporate a plurality of separate display panels as taught by AAPA in the display device taught by Nakao, as modified by Liaw, in order to produce a modular display which encompasses several panels that are easier to repair.

As to <u>Claim 7</u>, Nakao, as modified by Liaw, fails to teach that the display panel includes a plurality of small display panels that are bonded together so that display screens of the plurality of small display panels are on a same plane. Examiner cites AAPA to teach a plurality of small display panels that are bonded together so that display screens of the plurality of small display panels are on a same plane (See Fig. 13 and Pg. 3, second to last paragraph—Pg. 5, second to last paragraph). At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to incorporate a plurality of small display panels that are bonded together so that display screens of the plurality of small display panels are on a same plane as taught by AAPA in the display device taught by Nakao, as modified by Liaw, in order to produce a modular display which encompasses several panels that are easier to repair.

10/797,000

Art Unit: 2629

As to Claim 9, most of the claim limitations have already been addressed with respect to claim 8 with the exception of the display panel including a plurality of thin-film transistor panels and that the plurality of thin-film transistor panels are bonded together so that display screens of the plurality of thin-film transistor panels are on a same plane. Nakao, as modified by Liaw, fails to teach the display panel including a plurality of thinfilm transistor panels and that the plurality of thin-film transistor panels are bonded together so that display screens of the plurality of thin-film transistor panels are on a same plane. Examiner cites AAPA to teach the display panel including a plurality of thin-film transistor panels and that the plurality of thin-film transistor panels are bonded together so that display screens of the plurality of thin-film transistor panels are on a same plane (See Fig. 13 and Pg. 3, second to last paragraph—Pg. 5, second to last paragraph). At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to incorporate a plurality of thin-film transistor panels that are bonded together so that display screens of the plurality of thin-film transistor panels are on a same plane as taught by AAPA in the display device taught by Nakao, as modified by Liaw, in order to produce a modular display which encompasses several panels that are easier to repair.

4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nakao and Liaw in view of Nakamura (USPGPUB 2003/0043132—hereinafter "Nakamura").

As to Claim 5, Nakao, as modified by Liaw, fails to teach that the reference voltage generating section includes a plurality of reference voltage generating circuits 10/797,000 Art Unit: 2629

that are respectively for colors used for performing color display of the image. Examiner cites Nakamura to teach that the reference voltage generating section includes a plurality of reference voltage generating circuits that are respectively for colors used for performing color display of the image (*Fig. 2, 20*). At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to incorporate separate color voltage reference generators as taught by Nakamura in the display device taught by Nakao, as modified by Liaw, in order to generate the gradation reference voltage group for each specific color, thereby controlling each color separately in order to enhancing the optical characteristics of the display device (*Nakamura—Pg. 3, ¶ 41*).

Response to Arguments

5. Applicant's arguments with respect to claims 1-11 have been considered but are moot in view of the new ground(s) of rejection.

10/797,000 Art Unit: 2629

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Inquiries

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rodney Amadiz whose telephone number is (571) 272-7762. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

10/797,000 Art Unit: 2629

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

RA.

R.A. 12/6/07

Division 2629

SUMATI LEFKOWITZ SUPERVISORY PATENT EXAMINER